

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 10-31-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: System and Method for Securing
Using Decryption Keys During FPGA
Configuration Using a
Microcontroller
Attorney Docket Number:: X-1435 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 03
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Stephen
Middle Name:: M.
Family Name:: Trimberger
Street:: 1261 Chateau Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95120

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

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| Representative Customer Number:: | 24309 | |
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124